

CICS TS for VSE/ESA Performance Tuning Tips

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CICS TS Performance Tuning Tips

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CICS TS for VSE/ESA Performance Tuning Tips

Topics

- Definition of performance and tuning
- CICS performance constraints
- Options to reduce constraints
- Monitoring CICS performance
- Summary

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Definitions

Performance

The overall quality of service and operations of a given system as determined by ease-of-use, availability, response time, and throughput

Performance Evaluation

The analysis of such factors as throughput rate, turnaround time, and constrained resources to determine how well a system is meeting specific processing requirements

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Definitions...

Constraint

A place in the system where contention for a resource is affecting performance, sometimes referred to as "transaction throughput degradation" or bottleneck.

Tuning

The process of adjusting system control variables to make the system divide its resources most efficiently for the workload

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CICS TS for VSE/ESA Performance Tuning Tips

CICS Performance Constraints

- Hardware
 - CPU cycles
 - Real storage
 - I/O
 - DASD
 - Network
- Software
 - Software specifications
 - Virtual storage

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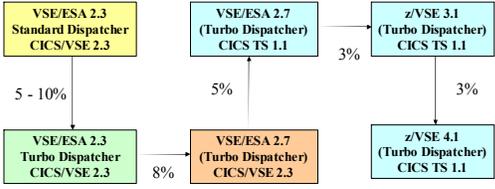
Hardware - CPU Cycles

- VSE/ESA 2.4+ supports Turbo Dispatcher only
 - Uses more CPU time than standard dispatcher
- CICS TS uses more CPU than CICS/VSE
- Review current CPU usage
 - IUI system status
 - CPUMON tool (download from z/VSE website)
 - Vendor monitoring products

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Hardware - CPU Cycles...

- CPU Time Requirements
Source: VSE Performance Considerations documents



```

    graph TD
      A["VSE/ESA 2.3  
Standard Dispatcher  
CICS/VSE 2.3"] -- "5 - 10%" --> B["VSE/ESA 2.3  
Turbo Dispatcher  
CICS/VSE 2.3"]
      C["VSE/ESA 2.7  
Turbo Dispatcher  
CICS/VSE 2.3"] -- "8%" --> B
      D["VSE/ESA 2.7  
Turbo Dispatcher  
CICS TS 1.1"] -- "5%" --> C
      E["z/VSE 3.1  
Turbo Dispatcher  
CICS TS 1.1"] -- "3%" --> D
      F["z/VSE 4.1  
Turbo Dispatcher  
CICS TS 1.1"] -- "3%" --> E
    
```

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CICS TS for VSE/ESA Performance Tuning Tips

Hardware - CPU Cycles...

- Solutions to processor constraint problems
 - Limit number of concurrent tasks in CICS
 - Lower MXT value
 - Use transaction classes

```
CEDA DEFINE TRANCLASS(CLASS01) MAXACTIVE(5)
CEDA DEFINE TRANSACTION ... TRANCLASS(CLASS01)
```

- Replaces DFHSIT CMXT and PCT TCLASS parameters in CICS/VSE 2.3



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Hardware - CPU Cycles...

- Solutions to processor constraint problems
 - Reduce trace overhead
 - Turn off system tracing
 - Use CICS TS special tracing by transaction or terminal
 - Increase CICS TS partition priority
 - Implement Shared Data Tables



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Hardware - CPU Cycles...

Activating CICS TS Special Tracing

Item	Choice	Possible choices
Internal Trace Status	==> STARTED	STARTed, STOPped
Internal Trace Table Size	==> 256 K	16K - 1048576K
Auxiliary Trace Status	==> STOPPED	STARTed, STOPped, Paused
Auxiliary Trace Dataset	==> A	A, B
Auxiliary Switch Status	==> NO	NO, NExt, All
Master System Trace Flag	==> ON	ON, OFF ← Set to OFF
Master User Trace Flag	==> ON	ON, OFF ← Set to OFF

When finished, press ENTER.

PF1=Help 3=Quit 4=Components 5=Ter/Trn 9=Error List



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Hardware - CPU Cycles...

Activating CICS TS Special Tracing

Item	Choice	Possible choices
Transaction ID	====> ABCD	Any valid 4 character ID
Transaction Status	====> SPECIAL	Standard, Special, Suppressed
Terminal ID	====> NB05	Any valid Terminal ID
Netname	====> TELM05	Any valid Netname
Terminal Status	====> SPECIAL	Standard, Special
Terminal ZCP Trace	====> OFF	ON, OFF

When finished, press ENTER.

PF1=Help 3=Quit 9=Error List

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Hardware – Real Memory

- May require more real memory
- Virtual storage requirements are larger
 - CICS TS 31-bit partition GETVIS
 - Minimum 12.5MB plus VSAM buffer requirements
 - 50MB in VSE/ESA environment B ALLOC proc
 - More data space usage
 - Basic Security Manager
 - CICS Data Management Facility (DMF)
 - CICS Shared Data Tables
 - Environment B SYSDEF DSIZE=20MB

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Hardware – Real Memory...

- Exploiting more 31-bit virtual will increase real storage requirements
- Ideal paging rate for CICS system is zero
 - Review paging rates before migrating
 - SIR command
 - IUI system status dialog
 - Vendor monitoring product

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Hardware – Real Memory...

- Load CICS TS phases in SVA
 - DFHSIT SVA=YES (default is NO)
 - Not an option if running CICS/VSE 2.3 partition
- Reduce VSAM buffer requirements
 - Use LSR pools or fewer buffers
- Limit number of concurrent tasks in CICS
 - Lower MXT value

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Hardware – DASD I/O

- Reduce number of I/O requests
 - CICS VSAM files
 - User VSAM files
 - CICS system files
 - Temporary Storage
 - Transient data
 - CSD file
 - Tune VSAM IDCAMS definitions
 - Large data control interval sizes (8K or greater)
 - Avoid Shareoption 4
 - Use FREESPACE for KSDS files to avoid CI/CA splits

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Hardware – DASD I/O

- Reduce number of I/O requests ...
 - More data in memory
 - Use LSR pools
 - Index buffers are now separate from data buffers
 - Increase VSAM index and data buffers
 - More index buffers for random processing
 - More data buffers for sequential processing
 - VSAM buffers in 31-bit storage
 - Use Shared Data Tables

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CICS TS for VSE/ESA Performance Tuning Tips

Hardware – DASD I/O...

- Reduce number of I/O requests
 - Minimize program compression and loading
 - Make application programs 31-bit enabled
 - Use virtual disk for program load library
 - Most active libraries first in LIBDEF
- Reduce I/O service times
 - Multiple control units
 - Multiple channel paths
 - DASD caching
 - Faster DASD

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Software - specifications

- Waits caused by task parameters
 - MXT
 - Limits total number of user tasks in CICS partition
 - CICS TS pre-allocates storage based on MXT
 - Don't use 999
 - Transaction class
 - Limits total number of user tasks by class name
 - IBM supplied definitions DFHTCL01 – DFHTCL10 for transaction classes 1-10
 - MAXACTIVE default is 1

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Software - specifications...

- Waits caused by task parameters
 - Transaction processing priority
 - Three-digit value less than or equal 255
 - Transaction priority + terminal priority + operator priority
 - Priority aging
 - Mechanism to keep low priority tasks from being stranded
 - SIT PRYAGE=32768|nnnnn (milliseconds)
 - Transaction priority increase by 1 every nnnnn ms.

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CICS TS for VSE/ESA Performance Tuning Tips

Software - specifications...

- Waits caused by CICS VSAM definitions
 - Avoid wait on VSAM strings and buffers
 - User VSAM files in FCT
 - STRNO, BUFNI and BUFND parameters
 - LSR buffer pools
 - STRNO, index and data buffer specifications
 - Transient data and temporary storage datasets
 - SIT TS=(buffers, strings)
 - SIT TD=(buffers, strings)
 - CSD file
 - SIT CSDSTRNO, CSDLRNO

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Software - specifications...

- Waits caused by CICS VSAM definitions
 - Avoid NOSPSPACE condition
 - Transient data and temporary storage datasets
 - Define secondary allocation or monitor space usage

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Software – virtual storage

- CICS TS Partition
 - Most of CICS nucleus above 16 MB line
 - All major CICS control blocks above 16 MB line
 - 8 Dynamic Storage Areas (DSA)
 - 4 DSAs above 16 MB line in extended (31-bit) DSA (EDSA)
 - 4 DSAs below 16 MB line in 24-bit DSA

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Software – virtual storage...

- Parameters to exploit 31-bit storage
 - Transaction definition
 - Program definition
 - EXEC CICS GETMAIN requests
 - Program's addressing mode (AMODE) and residency mode (RMODE)
 - SIT options

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Software – virtual storage...

- Transaction definition parameters
 - Controls DSA used for task lifetime storage
 - TASKDATALOC(value)
 - BELOW 24-bit DSA
 - ANY either 31-bit or 24-bit DSA
 - Program must be linked AMODE(31)

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Software – virtual storage...

- Program definition parameters
 - Controls DSA used for EXEC commands with SET option
 - DATALOCATION(value)
 - BELOW 24-bit DSA
 - ANY either 31-bit or 24-bit DSA
 - Application program must be linked AMODE(31)

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Software – virtual storage...

- Program definition parameters
 - EXEC CICS GETMAIN with FLENGTH option
 - Acquired in 24-bit DSA if program linked AMODE(24)
 - Acquired in 31-bit DSA if program linked AMODE(31)
 - Program linked RMODE(ANY)
 - Program loaded in 31-bit or 24-bit DSA

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Software – virtual storage...

- SIT options to exploit 31-bit storage
 - TCT User Area (TCTUA)
 - SIT TCTUALOC=BELOW|ANY
 - BELOW 24-bit DSA
 - ANY 31-bit or 24-bit DSA
 - Application programs addressing TCTUA must be linked AMODE(31)

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Shared Data Tables

- Data in memory option
- High performance file access
 - Read operations
 - Full key, imprecise key, and browse
 - FCT or RDO option DATATABLE=CMT|UMT
- Extends previous support in CICS/VSE
- Data Table now in VSE Data Space
 - Owned by FOR

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Shared Data Tables...

- Can be shared between CICS TS partitions in same VSE system
 - Cross memory services for read data access
 - Requires MRO between CICS partitions
 - Control functions
 - File updates

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Shared Data Tables...

CICS/VSE Support

The diagram illustrates the CICS/VSE support architecture. It features a 31-bit shared area at the top and a 24-bit shared area at the bottom. Two CICS partitions, CICS1 and CICS2, are shown. CICS1 contains a local FCT and an Application. CICS2 contains a remote FCT and an Application. A Data Table is shared between them. MRO Function Shipping is indicated between the two partitions.

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Shared Data Tables...

CICS TS Support

The diagram illustrates the CICS TS support architecture. It features a 31-bit shared area at the top and a 24-bit shared area at the bottom. Two CICS partitions, CICS1 and CICS2, are shown. CICS1 contains a local FCT and an Application. CICS2 contains a remote FCT and an Application. A Data Table is shared between them. MRO (control and updates only) is indicated between the two partitions. Cross memory services Reads are also shown.

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Monitoring CICS Performance...

LSR pool statistics

```

LSR Pools
Pool Number : 1    Time Created : 15:52:07.37886
-----
Maximum key length . . . . . 22
Total number of strings . . . . . 10
Peak concurrently active strings : 1
Total requests waited for string : 0
Peak requests waited for string : 0
Buffer Totals
-----
Data Buffers . . . . . 28
Successful look asides . . . . . 521
Buffer reads . . . . . 363
User initiated writes . . . . . 269
Non-user initiated writes . . . . . 0
Index Buffers . . . . . 0
Successful look asides . . . . . 0
Buffer reads . . . . . 0
User initiated writes . . . . . 0
Non-user initiated writes . . . . . 0
Data and Index Buffer Statistics
-----
              Look              User
Size Buffers Asides Reads      Writes Writes
-----
512      8         54        13      0      0
2048     6          0       155     195    0
4096    14         487       195     74     0
    
```

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Monitoring CICS Performance...

- Options for collecting/reporting statistics
 - Data Management Facility (DMF)
 - Statistics recorded automatically or at user request
 - Print using DFHSTUP
 - User program for selected statistics
 - System Programming Interface command
 - At user request
 - Sample program DFH0STAT
 - Output to VSE/POWER LST queue or TS
 - At user request

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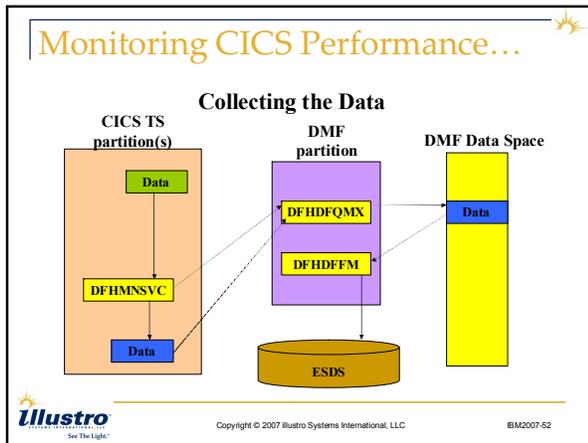
Monitoring CICS Performance...

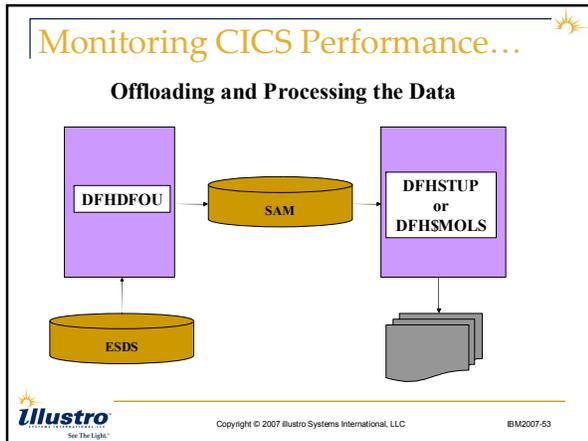
- What gets recorded by DMF
 - Automatically
 - Interval Statistics
 - Only with initialization parameter STATRCD=ON
 - User specified interval - default is 3 hours
 - Calculated forward from midnight (3 AM, 6 AM, 9 AM, etc)
 - End of Day Statistics
 - User specified - default is midnight
 - Shutdown - normal or immediate
 - Unsolicited Statistics
 - For dynamically allocated and de-allocated resources
 - Files, LSRPOOLS, transactions, programs, etc.

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- Monitoring CICS Performance...**
- **Sample program DFHOSTAT.C**
 - COBOL for VSE/ESA source in PRD1.BASE
 - Uses EXEC CICS COLLECT STATISTICS commands
 - Output to
 - VSE/POWER LST queue using Report Controller
 - CICS Temporary Storage queue
 - Can be executed
 - From terminal
 - From PLT during CICS shutdown
 - As a STARTed transaction
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CICS TS for VSE/ESA Performance Tuning Tips

Summary

- Performance of CICS TS system depends on many factors
- Similar tuning options as CICS/VSE 2.3 but several new options
- More support to improve CICS performance
 - 31-bit storage exploitation
 - Shared Data tables

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Other Sources of Information

- CICS TS Performance Guide
- IBM Redbooks
 - Migration to VSE/ESA 2.4 and CICS Transaction Server for VSE/ESA 1.1 (SG24-5595)
 - Implementation of VSE/ESA 2.4 and CICS Transaction Server for VSE/ESA 1.1 (SG24-5624)
- CICS Transaction Server Website
 - www-4.ibm.com/software/ts/cics
 - Manuals, flyers, brochures, etc.

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